

20/3/19

U.S. UTILITY Patent Application

**PATENT NUMBER and
ISSUE DATE**

APPL NUM 10052244	FILING DATE 01/23/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER <u> </u>
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APPLICANTS: Sasaki Katsuhito

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--CONTINUING DATA VERIFIED:

FOREIGN APPLICATIONS VERIFIED:

JAPAN 250752/2001 08/21/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed 35 USC 119 condition(s) met		<input type="checkbox"/> yes <input type="checkbox"/> no	OKI 295
Verified and Acknowledged Examiner's initials			
TITLE : Method of manufacturing lateral double-diffused metal oxide semiconductor device			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE			DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Fig.s Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	Application Examiner	
			PREPARED FOR ISSUE	
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